

2112
#6
J. Douglas
3/18/99

Docket No.: 42390.P5319

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application^{or}:

Dunton, et

Application No. 08/984,005

Filed: December 3, 1997

For: METHOD AND APPARATUS
FOR PROCESSING DIGITAL
PIXEL OUTPUT SIGNALS

Examiner: Not Yet Assigned

Art Group: 2712

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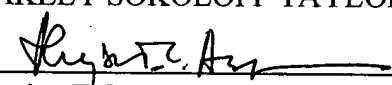
Dear Sir:

Enclosed herewith for filing in the above-identified U.S. patent application are the formal drawings, Figures 1 - 3 (submitted on 3 pages).

Respectfully submitted,

BLAKELY SOKOLOFF TAYLOR & ZAFMAN, LLP

Date: 2/1, 1999


Aloysius T.C. AuYeung
Reg. No. 35,432

12400 Wilshire Boulevard
Seventh Floor
Los Angeles, CA 90025
(503) 684-6200

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FIG. 1

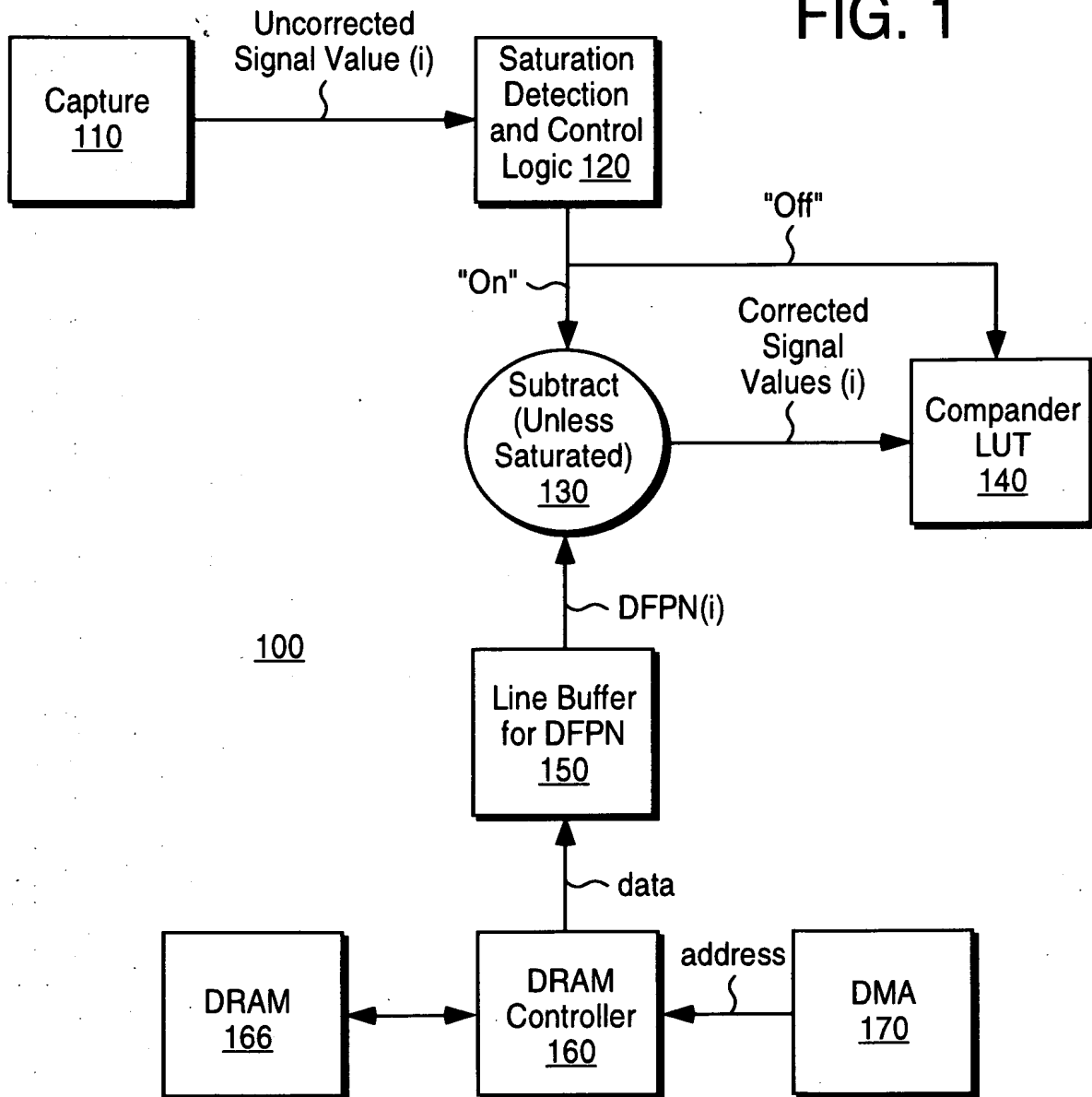
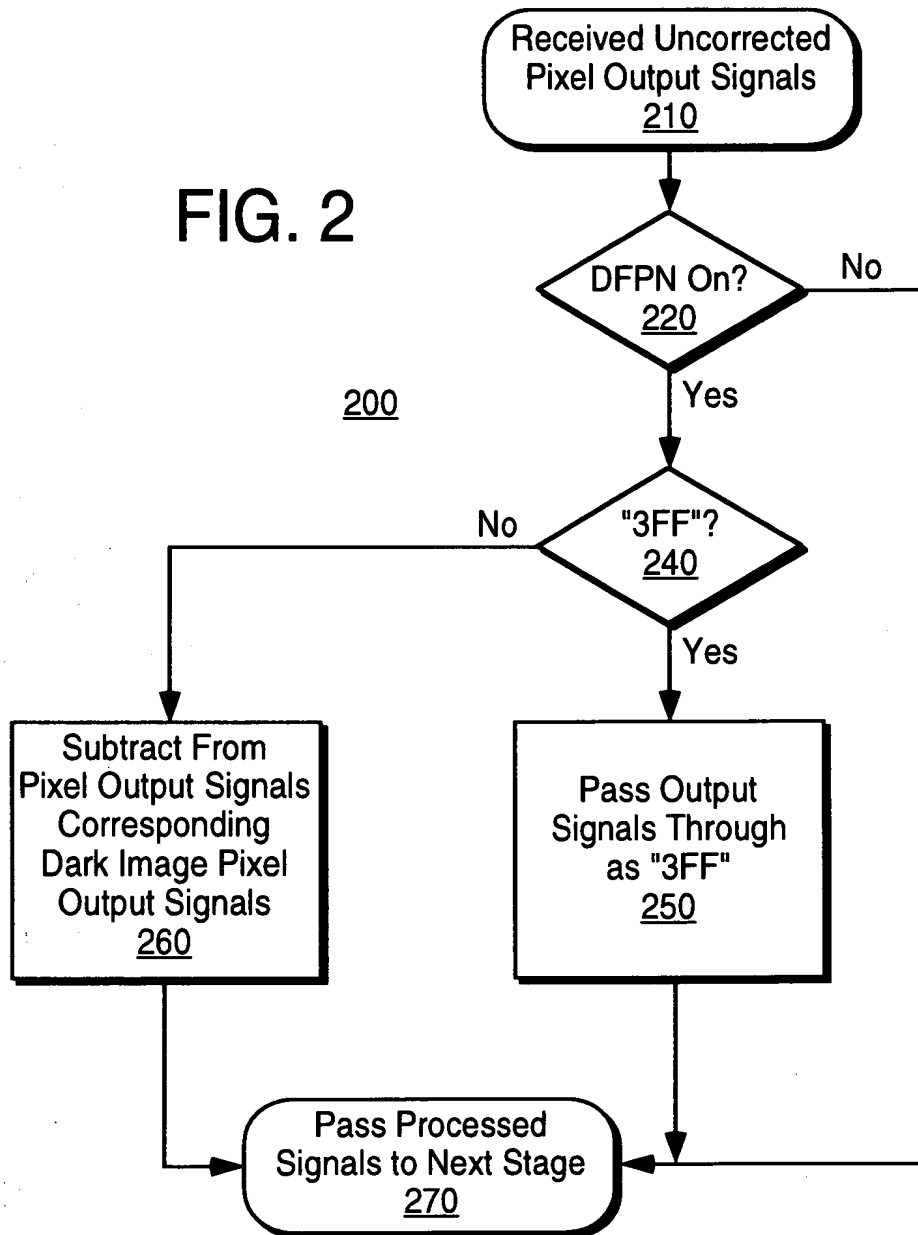


FIG. 2



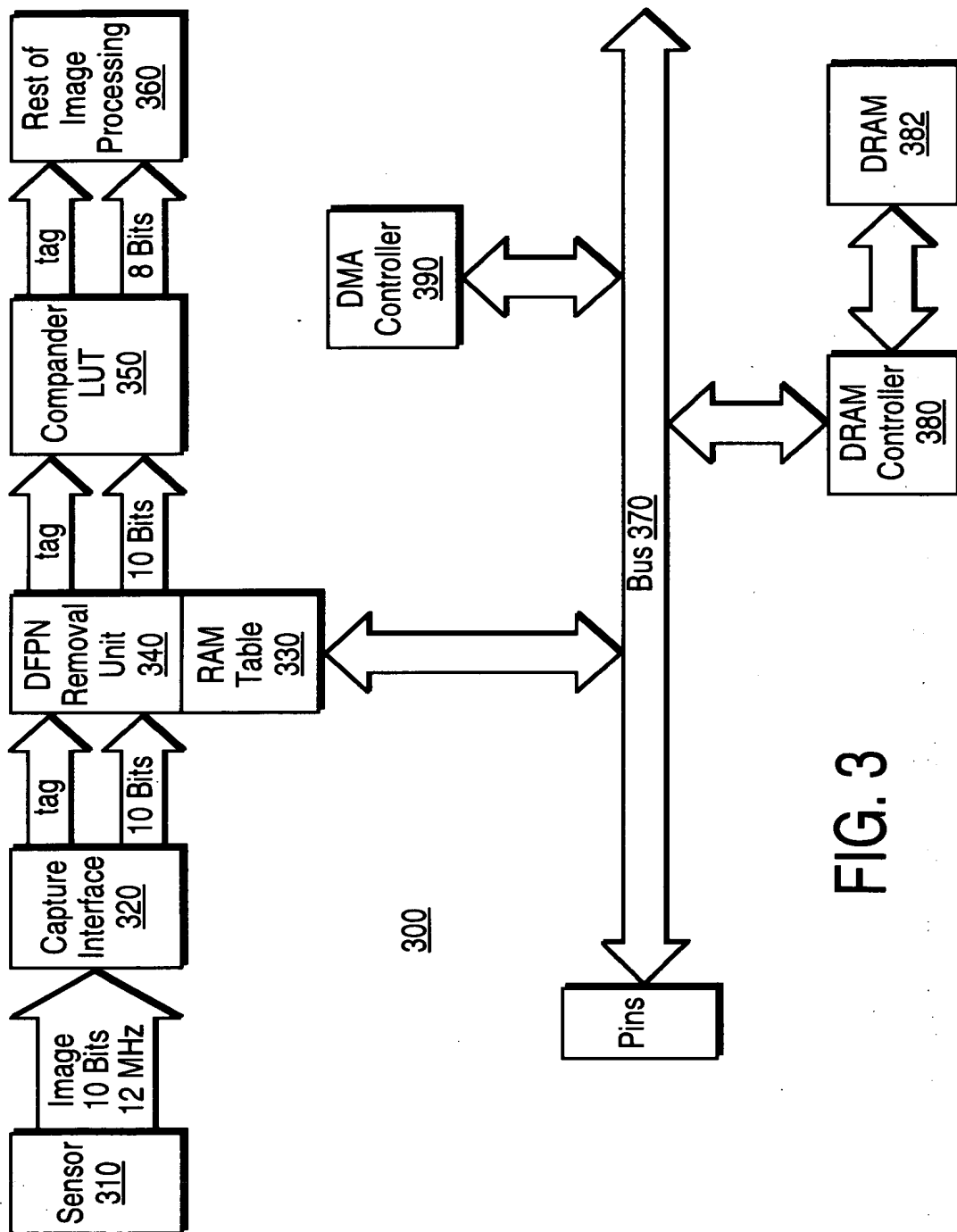


FIG. 3